



Department of Electronic Science

University of Delhi South Campus
Benito Juarez Road, New Delhi-100021
Fax No. 24110440 Tel. No. 24110440

UDSC/DES/2015/ 534

Dated: 06-01-2016

Sealed quotations are invited for **Hardware Bench Top for Digital /Analog Circuits** (Indian Rupees or foreign currency) for supply and installation at Department of Electronic Science of the item as described below:

S. No.	Name of the Item	Description & Specification	Quantity
1.	Hardware Bench Top for Digital /Analog Circuits	<p>Hardware Bench Top for Digital /Analog Circuits should consist of built in measuring instruments with required Technical Specifications given below:</p> <ol style="list-style-type: none">Digital Multimeter 5 ½ digit.Multifunction 8 Channel Digital Storage Oscilloscope with Sampling rate of 1 Ms/sFunction Generator minimum upto 1MHzArbitrary Waveform Generator+/-12V Variable Power Supply+/-15V, 5V Constant Power SupplyBode Analyzer upto 5MHzSignal AnalyzerImpedance Analyzer 1HzTwo-Wire current / voltage AnalyzerThree-Wire current / voltage Analyzer for NPN, PNPHardware Should support 3D Breadboarding in Multisim SoftwareCustomized instruments (Software defined) should be supportedHardware should support add on boards for optical communications, Digital/Analog Communications.Hardware should be totally compatible with multisim and lab view Software	10

Important Information:

1. The sealed quotation should be addressed to Head, Department of Electronic Science, University of Delhi South Campus, Benito Juarez Road, New Delhi - 110021, with all terms and conditions latest by **27-Jan-2016, 4:30 PM**.
2. Quotations have to be submitted in a two- bid system. The first part, Technical bid, should consist of all technical details and supporting documents with terms and conditions. The second part, Financial bid, should contain item-wise pricing of items mentioned in the technical bid. Both the quotations/ bids are to be sealed in separate covers duly super scribed by the supplier and then placed in a bigger cover with should also be sealed and super –scribed. The supplier shall bear all the cost associated with preparation and submission of the quotation including samples and related literature.
3. The bidder will have to quote all the items together: partial quotes will not be accepted. For each item, make model, technical specification and quantity have to be mentioned clearly.
 - Proprietary certificate, if the item is a proprietary item.
 - Manufacturer's Authorization Certificate.
 - The Company profile
 - Details of atleast 10 users in India for the item quoted in the bid.
 - Details of support/ service centers.
 - Validity compliance statement.
 - Deviation table.
 - Technical documentation (Printed product brochures and all technical leaflets, etc) to be compulsorily attached with the technical offer.
4. The quote should be valid for 90 days from due date. Payment will be by DD or wire transfer upon installation.

**5. Date, Time and Venue of
Technical Bid opening:**

**28-01-2016, 11:00 AM
Department of Electronic Science, UDSC**

Anakshi Khular

Head
Department of Electronic Science
University of Delhi South Campus

Professo: Anakshi Khular Sharma
Head
Department of Electronic Science
University of Delhi South Campus
New Delhi- 110021, India