

# Department of Electronic Science

## Sixth Admission List

### Schedule of Admission for M. Sc. (Electronics)

Sixth Admission List Announcement – Verification of Documents, Approval of Admission and Payment of online admission fee	August 21, 2017 – August 24, 2017
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Note: -

1. For document verification and approval of admission official working hours between 10:00 am to 5:00 pm shall be observed.
2. After approval of admission, the applicant has to log on the **PG admission portal** to make online admission fee payment.

*Avinashi Kapoor*  
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Professor Avinashi Kapoor  
Head  
Department of Electronic Science  
University of Delhi South Campus  
New Delhi-110021



## Category : ST (Entrance Based)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Roll No.	Form No.	Name	Alloted Department/College	Final Entrance Marks	Combined Rank	Qualifying Marks %/GP	Category Filled by Candidate
1	10510163	17ESCI1032040	LALCHHANHIMI	Department of Electronics	32	207	60.25	ST

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14.8.17