

## Department of Electronic Science Fourth Admission List

### Schedule of Admission for M. Sc. (Electronics)

Fourth Admission List Announcement – Verification of Documents, Approval of Admission and Payment of online admission fee	August 10, 2017 – August 14, 2017
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Note: -

1. For document verification and approval of admission official working hours between 10:00 am to 5:00 pm shall be observed.
2. After approval of admission, the applicant has to log on the **PG admission portal** to make online admission fee payment.

*Avinashi Kapoor*

Professor Avinashi Kapoor 17  
Head  
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New Delhi-110021

## Category : UNRESERVED (Entrance Based)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Roll No.	Form No.	Name	Alloted Department/College	Final Entrance Marks	Combined Rank	Qualifying Marks %/GP	Category Filled by Candidate
1	10510056	17ESCI1093938	ASHISH SHARMA	Department of Electronics	94	10		Unreserved

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## Category : ST (Entrance Based)

Note : In case of the tie at the last seat(s) the information of the candidates are displayed at the end of this admission list.

S.No.	Roll No.	Form No.	Name	Alloted Department/College	Final Entrance Marks	Combined Rank	Qualifying Marks %/GP	Category Filled by Candidate
1	10510087	17ESCI1074300	LALIT	Department of Electronics	39	173	62.16	ST

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